

Electronic Acknowledgement Receipt

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Application Number:	10631921
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Title of Invention:	TRANSISTOR GATE AND LOCAL INTERCONNECT
First Named Inventor/Applicant Name:	Jigish D. Trivedi
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Certificate of Correction	Certificate_of_Correction_MI CRON-104DV1.pdf	61214 c0d85264a74009e479fc7dcdf9c19f1ab0 9000887	no	2

Warnings:

Information:

2	Fee Worksheet (PTO-06)	fee-info.pdf	8150 2e186c18164b39537601976989011abc d2bc26f	no	2
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